Muhunthan K R

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**Career Objective**:

To work, grow and learn in an organization and to reach the organizational goals by utilizing my potential and skills at its fullest.

**Profile Summary** : A competent professional experience with 3 yrs.

3 years of experience in PCB Design, Hardware Testing, Assembly work, Fabrication, Component selection, BOM Creation & Management.

Key Skills :

**Schematic** : Creating Schematic in respective Design Tool as per hardware architecture and inputs.  
 **PCB Layout Design** : Knowledge & working experience in different stages of PCB Layout like Component importing, Board outline, Stack up, constraint or Rules setting, Placement, Routing & Manufacturing Output generation (Up to 6 Layers). And exposure to relevant Industry standards such as IPC-7351, IPC-2221.  
 **Assembly Work** : wave Soldering, Baking the board, RoHs and Non-RoHS board, BOM verification, Assembly documentation (Material List, IC baking hours as per IPC Standard, Special assembly instruction as per IPC-A-610) • Stencils: stencil work done for the PCBA Boards by using Oven Machine.  
 **Fabrication** : Fabricate work done for upto 4-layer board in own house itself.  
 **Hardware Testing** : Cold test, Power boot up test, Resistive impedance test and troubleshooting the failure board from fields and tools.  
 **Component Selection** : Passive, Active, IC’s, Power supply, Connector. etc., to alternate source FFF identification & Validation for obsolescence projects.  
 **BOM** : BOM Creation/ Management and Analysis.

Tools Used:

**Schematic Tools**  : OrCAD Capture .  
 **PCB Layout Tools** : Cadence Allegro 16.6,PADs.

CAREER SPAN:

**Organization** : Pinaka Aerospace Solutions Pvt. Ltd.

**Client** : DRDO .  
**Location** : Bangalore   
**Designation** : Hardware Design engineer  
**Duration** : March 2022 – Till Date

ROLES & RESPONSIBILITIES IN DRDO :

* Working on the System level design of the Project.
* Working on Schematic creation, netlist generation, Part list finalization Etc
* Working on replacing the component as per MIL standard with FFF or new design with same functionality replacement or alternative part.
* BOM creation, Design Analysis (Power budget analysis, Area analysis, derating analysis, Reliability analysis) Documentation.
* PCB Design, Component Foot print creation,Layout creation, Thermal Analysis and Gerber creation.

**Organization** : Dipitr Technolgies Pvt. Ltd.  
 **Location** : Bangalore.  
 **Designation** : Hardware Testing engineer.  
 **Duration** : September 2020 – February 2022.  
 **Project** : Strack Posture Corrector.

ROLES & RESPONSIBILITIES

* Working with electronic schematics, create 3D models for PCB footprints by using OrCAD Allegro.
* Working with engineering documentation systems and structures.
* Work with hardware engineers, PCB designers and fabricators for development and sustaining of actual products.
* Hardware Testing with all functionality testing And Acceptance testing.
* Develop and debug of custom scripting(production document) for automation and quality improvements.
* Creating Prototype for production and QC final product.

**Organization** : Greenmil International.  
 **Location** : Bangalore.  
 **Designation** : Hardware Testing engineer.  
 **Duration** : October 2019 – August 2020.  
 **project** : Function Generators & sensors.

ROLES & RESPONSIBILITIES

* Creating Prototype for Production BOM Checking .
* PCB Layout Checking and Assembled PCB Validation.

**Organization** : Thriveni sainik coal mining private limited.  
 **Location** : Hazaribagh.  
 **Designation** : Hardware Testing engineer.  
 **Duration** : April 2019 – August 2019.  
 **project** : 280 ton hitachi vehicle.

ROLES & RESPONSIBILITIES

* Maintaining Electric Drives.
* Verifying Harness, Sensors And Replacing damaged PCB components

**Organization** : Nano-Tek Electronic Engineer .  
 **Location** : Coimbatore.  
 **Designation** : Production engineer.  
 **Duration** : February 2017 – March 2019.  
 **project** : Temp Controllers, Power Panels and Sensors.

ROLES & RESPONSIBILITIES

* Assembling PCB,SMD & TH
* Functional Testing and Sensors Making.

Education Qualification

* Certification Course : Completed EMBEDDED SYSTEM in Vector India
* Completed BE(ECE) in Shree Venkateshwara Hi-Tech Engineering college,with 63% “April 2015”.
* Completed Higher Secondary Schooling in Saratha Higher secondary school,with 65% “April 2011”.
* Completed SSLC in Saratha Higher secondary school, with 83% “April 2009.

Personal Information

**Languages known :** English & Tamil. **Date of birth :** 19 May 1994. **Marital Status :** Married.

Declaration :

I hereby declare that all the details furnished above are true to the best of my knowledge and belief.

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